














All Tasks

HIRES Dewar/Detector Upgrade

Mon 7/28/03

ID	Task Name	Duration	Remaining Duration	Start	Finish	% Complete	Timeline																																											
							1	Half 1, 2002				Half 2, 2002				Half 1, 2003				Half 2, 2003				Half 1, 2004				Half 2, 2004				Half 1, 2005																		
							N	D	J	F	M	A	M	J	J	A	S	O	N	D	J	F	M	A	M	J	J	A	S	O	N	D	J	F	M	A	M	J	J	A	S	O	N	D	J	F	M	A	M	J
1	Science Grade CCDs (3)	160.4 days	62.47 days	Fri 10/11/02	Fri 6/6/03	61%																																												
2	Characterize and select CCDs	320 hrs	0 hrs	Fri 10/11/02	Mon 3/17/03	100%																																												
3	CCD/ backplane metrology	40 days	32 days	Mon 4/7/03	Fri 5/30/03	20%																																												
4	mount dead chips BP1	5 days	0 days	Mon 1/6/03	Fri 1/10/03	100%																																												
5	mount engineering grade chips BP2	5 days	0 days	Tue 4/15/03	Mon 4/21/03	100%																																												
6	mount science CCD hardware BP3	1 wk	1 wk	Mon 6/2/03	Fri 6/6/03	0%																																												
7	Documentation of existing dewar	30 days	0 days	Thu 12/27/01	Wed 2/6/02	100%																																												
8	Documentation of existing dewar	240 hrs	0 hrs	Thu 12/27/01	Wed 2/6/02	100%																																												
9	Conceptual Electronics Design	124 days	0 days	Fri 3/1/02	Wed 8/21/02	100%																																												
10	Dewar Design	264.5 days	0 days	Wed 4/3/02	Tue 4/22/03	100%																																												
11	Electronic design	153.5 days	0 days	Mon 7/1/02	Thu 2/13/03	100%																																												
12	CCD board	93.5 days	0 days	Mon 7/1/02	Thu 2/13/03	100%																																												
13	CCD board flex circuit	12 days	0 days	Mon 10/28/02	Tue 11/12/02	100%																																												
14	Mechanical Design	1238.9 hrs	0 hrs	Wed 4/3/02	Wed 11/6/02	100%																																												
15	Mechanical Fabrication	94 days	0 days	Wed 8/7/02	Tue 12/17/02	100%																																												
16	Fab of dewar body	6 wks	0 wks	Wed 8/7/02	Wed 9/18/02	100%																																												
17	Fab of back planes (3)	15 days	0 days	Mon 8/26/02	Fri 9/13/02	100%																																												
18	Fab Dewar Cold Finger	20 days	0 days	Wed 8/7/02	Wed 9/4/02	100%																																												
19	Fab of internal structures	66.5 days	0 days	Fri 9/13/02	Tue 12/17/02	100%																																												
20	Electronic Fabrication	3 wks	0 wks	Wed 4/2/03	Tue 4/22/03	100%																																												
21	Choise of LN2 or Cryo Tiger	0 days	0 days	Tue 6/11/02	Tue 6/11/02	100%																																												
22	Completion of Vacuum Dewar	0 days	0 days	Fri 12/20/02	Fri 12/20/02	100%																																												
23	Dewar electronics box	138.5 days	0 days	Mon 10/7/02	Thu 5/1/03	100%																																												
24	Electronic design	118.5 days	0 days	Mon 10/7/02	Thu 4/3/03	100%																																												
25	analog switch board	85.8 days	0 days	Mon 10/7/02	Fri 3/28/03	100%																																												
26	power board	12 days	0 days	Wed 1/8/03	Tue 4/1/03	100%																																												
27	internal flex circuits	3 wks	0 wks	Wed 2/5/03	Thu 4/3/03	100%																																												
28	Concept Mechanical design	200 hrs	0 hrs	Wed 11/13/02	Tue 12/17/02	100%																																												
29	Detailed Mechanical Design	27.75 days	0 days	Wed 1/8/03	Tue 4/1/03	100%																																												
30	Mechanical fabricaton	381 hrs	0 hrs	Fri 1/24/03	Wed 4/2/03	100%																																												
31	Electrical fabrication	4 wks	0 wks	Thu 4/3/03	Thu 5/1/03	100%																																												
32	Completion of Dewar System	0 days	0 days	Wed 4/2/03	Wed 4/2/03	100%																																												
33	Field Flattener	43 days	0 days	Mon 6/3/02	Wed 7/31/02	100%																																												
34	Optical fabrication	344 hrs	0 hrs	Mon 6/3/02	Wed 7/31/02	100%																																												
35	Completion of Field Flattener fab	0 days	0 days	Wed 7/31/02	Wed 7/31/02	100%																																												
36	Field Flattener coating	60 days	0 days	Mon 11/18/02	Fri 2/21/03	100%																																												
37	Coating at LL	60 days	0 days	Mon 11/18/02	Fri 2/21/03	100%																																												
38	Leach 2 vme Crate	203 days	8.4 days	Mon 7/8/02	Wed 4/30/03	96%																																												
39	Order parts/receive parts	6 wks	0 wks	Mon 7/8/02	Fri 8/16/02	100%																																												
40	Electronic fabricaton	100 hrs	0 hrs	Mon 9/2/02	Wed 9/18/02	100%																																												
41	software	240 hrs	24 hrs	Thu 3/20/03	Wed 4/30/03	90%																																												
42	VME Crate Complete	0 days	0 days	Fri 4/11/03	Fri 4/11/03	100%																																												
43	Leach 2 CCD Controler	211 days	11.74 days	Mon 7/8/02	Mon 5/12/03	94%																																												

Project: HIRES-3 chips
Date: Mon 7/28/03

Task		Summary		Rolled Up Progress		External Milestone	
Critical Task		Rolled Up Task		Split		Deadline	
Progress		Rolled Up Critical Task		External Tasks			
Milestone		Rolled Up Milestone		Project Summary	